



09/824135

Cofe  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Anthony M. Andric et al.

Assignee: Advanced Micro Devices, Inc.

Title: METHOD AND APPARATUS FOR DIRECT CONNECTION BETWEEN  
TWO INTEGRATED CIRCUITS VIA A CONNECTOR

Patent No.: 6,707,684

Issued: March 16, 2004

Atty. Docket No.: 1001-0144

**Certificate**

APR 28 2004

April 22, 2004

Certificate of Correction Branch  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**of Correction**

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT—  
PTO MISTAKE (37 C.F.R. § 1.322(a))**

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

1. Attached hereto, in duplicate, is Form PTO-1050, with at least one copy suitable for printing.
2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:  
Response to Final Office Action filed October 3, 2003; page 4, line 8 of claim 20.  
Response to Final Office Action filed October 3, 2003; page 5, lines 6 and 7 of claim 25.  
Examiner's Amendment in Notice of Allowance dated October 27, 2003; page 2, lines 16 and 17.

3. Please send the Certificate to:

Mark Zagorin  
ZAGORIN, O'BRIEN & GRAHAM, L.L.P.  
7600B N. CAPITAL OF TEXAS HWY, SUITE 350  
AUSTIN, TEXAS 78731

APR 29 2004

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Mark Zagorin", with a long horizontal flourish extending to the right.

Mark Zagorin, Reg. No. 36,067  
Attorney for Applicant(s)  
(512) 338-6311  
(512) 338-6301 (fax)

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO : 6,707,684

DATED : March 16, 2004

INVENTOR(S) : Anthony M. Andric, Ruel Hill, and Doug Markwardt

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 18, column 6, line 23, replace "arm" with -- are --

In claim 25, column 6, line 65, replace "act" with -- set --

In claim 25, column 6, line 66, replace "source" with -- surface --

In claim 26, column 8, line 7, replace "integrated circuit" with -- first --

MAILING ADDRESS OF SENDER:

ZAGORIN, O'BRIEN & GRAHAM, L.L.P.  
7600B N. Capital of Texas Hwy., Suite 350  
Austin, Texas 78731

PATENT NO. 6,707,684

APR 29 2004